

#1710  
1-15-02  
500.36904X00 R. Maib

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): MIURA, et al.

Serial No.: 09/254,939

Filed: March 17, 1999

For: METHOD OF FABRICATING SEMICONDUCTOR  
DEVICE HAVING TRENCH ELEMENT SEPARATION  
STRUCTURE

Group: 2814

Examiner: A. Mai

AMENDMENT

Assistant Commissioner for Patents  
Washington, D.C. 20231

December 28, 2001

Sir:

In response to the Office Action mailed August 28, 2001, please amend  
the above-identified application as follows:

IN THE CLAIMS

Please cancel claims 11 and 16 without prejudice or disclaimer, and  
amend the claims remaining in the application as follows:

1. (Thrice Amended) A method of fabricating a semiconductor device  
comprising the steps of:

12/31/2001 MBERHE 00000051 09254939

01 FC:103  
02 FC:102

396.00 OP  
168.00 OP